

1 RECEIVING APPARATUS

FIG.1

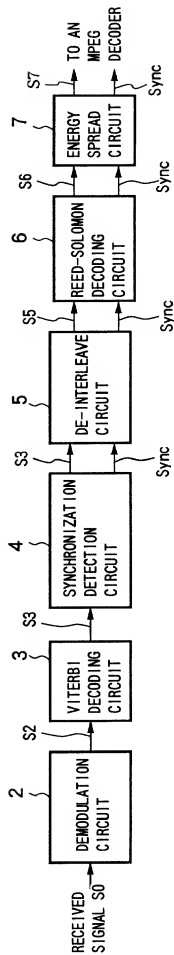
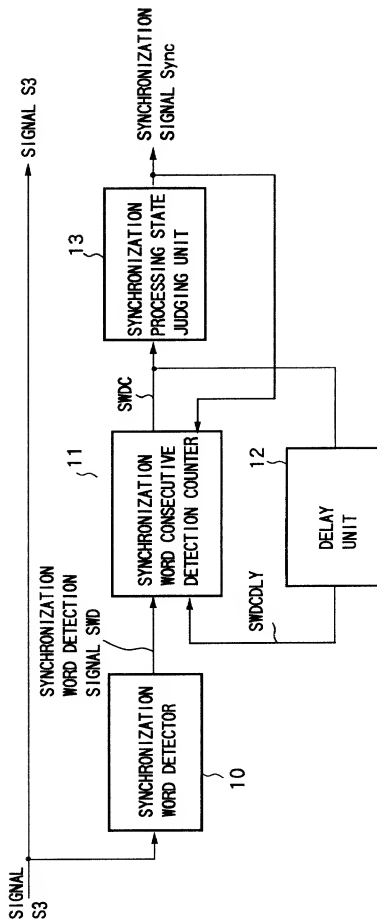
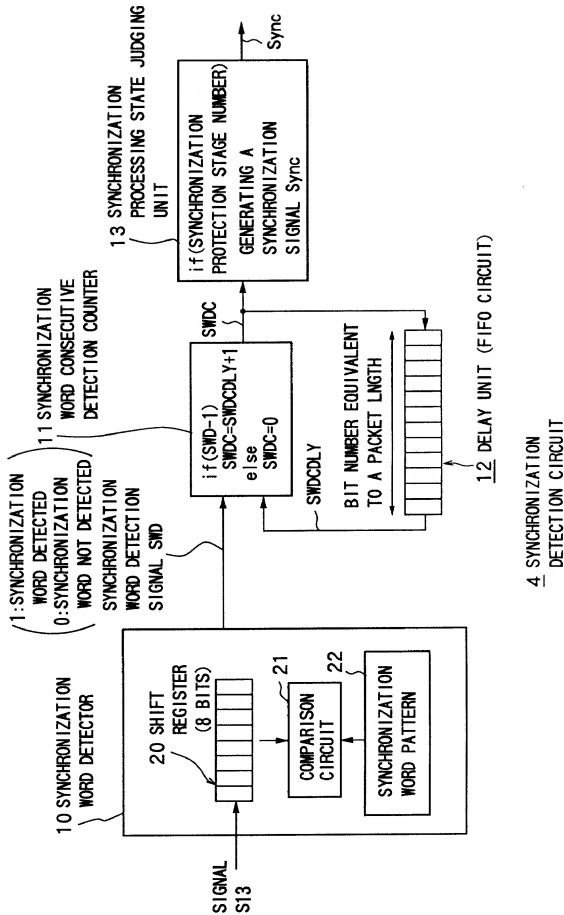


FIG. 2



4 SYNCHRONIZATION
WORD DETECTION CIRCUIT

FIG. 3



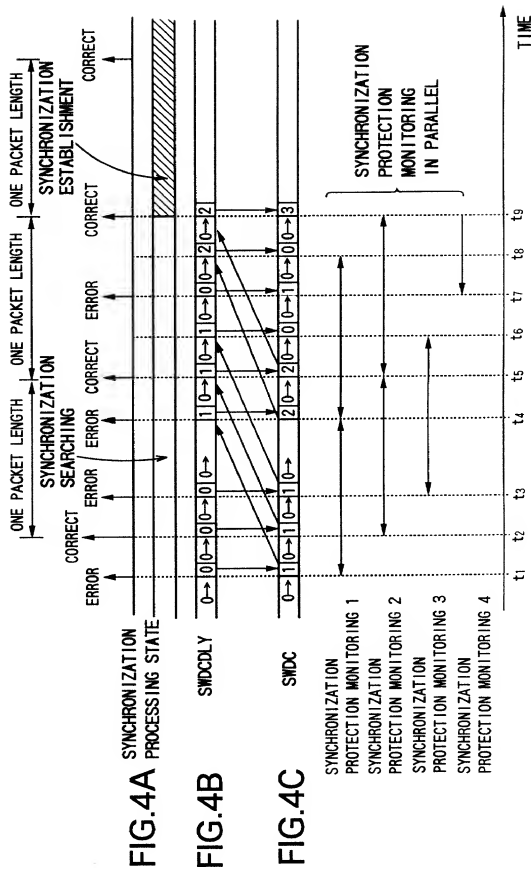
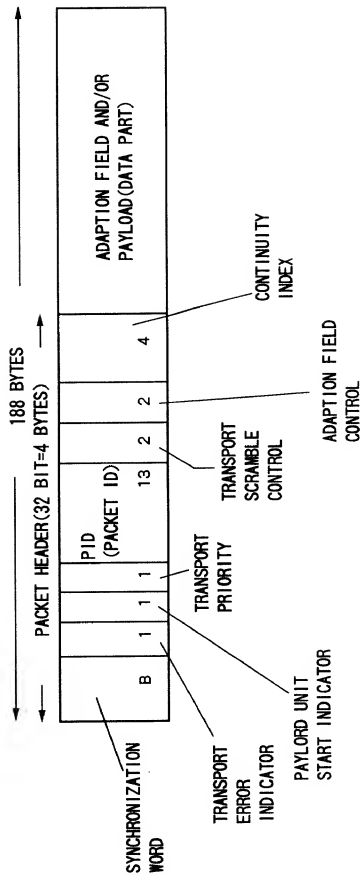


FIG.5



MPEG TRANSPORT STREAM

FIG.6A TRANSPORT
STREAM PACKET

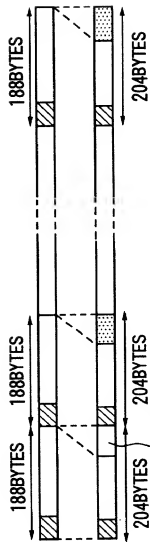
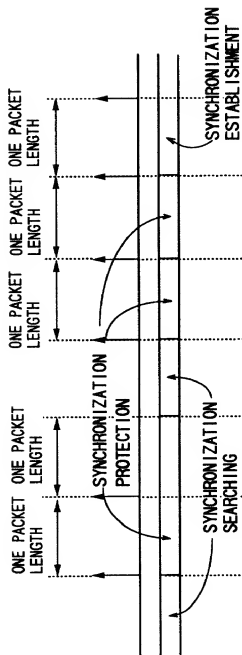
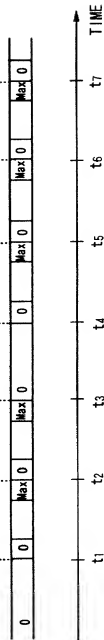


FIG.6B SLOT DATA

204BYTES
PARITY (16BYTES)

FIG. 7A
SYNCHRONIZATION
PROCESSING STATEFIG. 7B
SYNCHRONIZATION
PROTECTING COUNTFIG. 7C
PACKET LENGTH
COUNTER

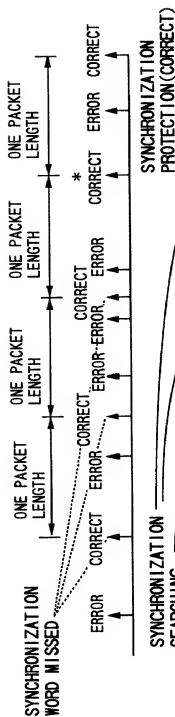


FIG. 8A SYNCHRONIZATION PROCESSING STATE



FIG. 8B

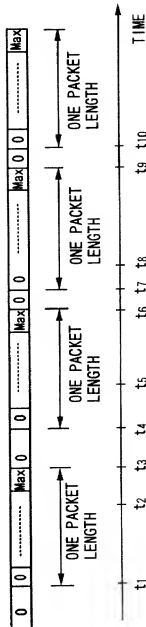


FIG. 8C